

The present application is a continuation of an application entitled "Graphics Processing Unit with an Integrated Fog and Blending Operation" which was filed December 5, 2000 under serial number 09/730,652 and attorney docket number NVIDP008A, and issued under US. Pat. No. 6,342,888; which was, in turn, a continuation of an application entitled "Transform, Lighting and Rasterization System Embodied on a Single Semiconductor Platform" which was filed on December 06, 1999 under serial number 09/454,516 and attorney docket number NVIDP008, and issued under US. Pat. No. 6,198,488. The present application is further related to applications filed coincidentally herewith under serial numbers 09/961,219, 09/961,228, and 09/957,746. The present application is further related to applications entitled "Method, Apparatus and Article of Manufacture for Area Rasterization using Sense Points" which was filed on December 06, 1999 under serial number 09/455,305, and attorney docket number NVIDP005, "Method, Apparatus and Article of Manufacture for Boustrophedonic Rasterization" which was filed on December 06, 1999 under serial number 09/454,505, and attorney docket number NVIDP006, "Method, Apparatus and Article of Manufacture for Clip-less Rasterization using Line Equation-based Traversal" which was filed on December 06, 1999 under serial number 09/455,728, and attorney docket number NVIDP007, "Method, Apparatus and Article of Manufacture for a Vertex Attribute Buffer in a Graphics Processor" which was filed on December 06, 1999 under serial number 09/454,525, and attorney docket number NVIDP009, "Method, Apparatus and Article of Manufacture for a Transform Module in a Graphics Processor" which was filed on December 06, 1999 under serial number 09/456,102, and attorney docket number NVIDP010, "Method and Apparatus for a Lighting Module in a Graphics Processor" which was filed on December 06, 1999 under serial number 09/454,524, and attorney docket number NVIDP011, and "Method, Apparatus and Article of Manufacture for a Sequencer in a Transform/Lighting Module Capable of Processing Multiple Independent Execution Threads" which was filed on December 06, 1999 under serial number 09/456,104, and attorney docket number NVIDP012 which were filed concurrently herewith, and which are all incorporated herein by reference in their entirety.